AZ-Delivery
Ihr Experte für Mikroelektronik!

Top board (left side):

		RST	
RTC-0	ADC1-0	GPIO36	SVP
DAC 2	RTC-7	ADC2-9	GPIO26	IO26
	CLK	GPIO18	IO18
	MISO	GPIO19	IO19
	MOSI	GPIO23	IO23
	SS	GPIO5	IO5
	+3.3V	3V3
Touch 4	SPI-HD	ADC2-4	GPIO13	TCK
SPI D3	GPIO10	SD3

Top board (right side):

TXD	GPIO3	U0 TX
RXD	GPIO1	U0 RX
IO22	GPIO22	I2C1 CL
IO21	GPIO21	I2C1 DA
IO17	GPIO17	U2 TX
IO16	GPIO16	U2 RX
GND		
VCC	+5V	
TD0	GPIO15	ADC2-3	RTC-13	Touch 3	SD CMD	HSPICS
SD0	GPIO7	SPI D0

RST

L
ON

Bottom board (left side):

GND			
NC			
RTC-3	ADC1-3	GPIO39	SVN
RTC-5	ADC1-7	GPIO35	IO35
Touch 8	RTC-8	ADC1-5	GPIO33	IO33
RTC-4	ADC1-6	GPIO34	IO34
HSPICL	SD CLK	Touch 6	RTC-16	ADC2-6	GPIO14	TMS
NC			
SPI D2	GPIO9	SD2
SPI CMD	GPIO11	CMD

Bottom board (right side):

GND					
IO27	GPIO27		RTC-17	Touch 7
IO25	GPIO25	ADC2-8	RTC-6	DAC 1
IO32	GPIO32	ADC1-4	RTC-9	Touch 9
TDI	GPIO12	ADC2-5	RTC-15	Touch 5	SD D2	HSPIQ
IO4	GPIO4	ADC2-0	RTC-10	Touch 0	SD D1	HSPIHD
IO0	GPIO0	ADC2-1	RTC-11	Touch 1
IO2	GPIO2	ADC2-2	RTC-12	Touch 2	SD D0	HSPIWP
SD1	GPIO8	SPI D1
CLK		SPI CLK

RST

L
ON

Legend

- Digital In/Out ports (all support PWM)
- Digital Input ports
- Analog Input 12 bits, 0 to 3.3V
- Analog Output 8 bits, 0 - 3.3V
- Capacitive Touch Sensor ports
- I/O -pins from RTC ultra low power processor, usable in deep sleep mode
- SD card interface
- SPI bus for Flash-memory, do not use

The following pins show the default assignment. All these signals can be changed to any In/Out port.
This applies also to UART0 and UART1, which cannot be accessed in the default assignment.

- I2C bus (Wire)
- VSPI bus
- Serial interfaces
- HSPI bus